Method For Fabricating Microlens With Lithographic Process

TECHNICAL FIELD

[0001] The present invention relates to the field of semiconductor devices, and more specifically, to the fabrication of microlens structures using a lithographic process.

BACKGROUND

equipped with photo-sensitive electronic components, such as photo-diodes. The digital imaging systems typically capture light information in a series of pixels. Commonly, the pixels are arranged in an array of rows and columns, such as 1024 x 768 pixels. Each pixel is represented by at least one photo-sensitive component. In applications requiring the capture of color, color filters may be used to capture the specific colors of the received light, and each pixel may be represented by more than one photo-sensitive component.

[0003] Generally, a microlens guides the light to the photo-sensitive component, essentially acting as the collection point for the digital imaging system. A microlens is a tiny lens formed on a semiconductor chip above each photo-sensitive component. Because the collected light passes through the microlens, it is important that the microlens be free of as many defects as possible.

[0004] FIG. 1 is a cross-section view of a semiconductor wafer configured with a microlens imaging system, although other configurations are possible. Microlenses 110 are located on a planarization layer 112, which acts as a spacer. The microlenses 110 capture light and directs the light through color filters 114 to photo-diodes 116 formed in the active region 118 of a wafer. A second planarization layer 120 may be located between the color filters 114 and the active region 118. A bond pad area 122 and scribe line 124 are typically formed as indentations in the wafer.

Microlenses are generally formed by applying a layer of microlens material on a semiconductor chip. The microlens material, such as a mixture of ethyl lactate and propyleneglycol monoether ether acetate and a mixture of propyleneglycol ether acetate and phenolic resin, also acts as a photo-resist material, *i.e.*, the microlens material may be masked, exposed, and developed to remove unwanted microlens material. Suitable microlens material includes MFR 380 series and MFR344 series, manufactured by JSR Corporation of Tokyo, Japan.

[0006] After the microlens material has been applied, exposed, and developed, a reflow process is performed to cause the remaining microlenses to form the preferred uniform symmetrical lens shape.

[0007] In order to achieve the preferred uniform and symmetrical lens shape, it is desirable that the microlens material be applied uniformly. If the microlens material is not applied uniformly, the microlens material may not reflow evenly and may create a non-uniform lens. The non-uniform lens may cause a stripe defect, resulting in a signal deviation. In many applications, the signal deviation is too great for the devices to function satisfactorily.

[0008] The topography of the semiconductor chip has an effect on the uniformity of the microlens material. For example, if the topography of the semiconductor chip contains valleys or indentations, the indentations may cause a non-uniform distribution of the microlens material. In particular, the bond pad area and the scribe line has been known to cause an uneven distribution of microlens material.

[0009] Therefore, there is a need for a method for fabricating microlenses to reduce signal deviation caused by the semiconductor chip topography.

SUMMARY OF THE INVENTION

[0010] These and other problems are generally reduced, solved or circumvented, and technical advantages are generally achieved, by embodiments of the present invention which provides a method of fabricating microlens devices.

[0011] In one embodiment of the present invention, indentations, such as bond pad areas and scribe lines, are filled with a filler material. The filler material creates a flatter surface upon which a uniform layer of microlens material may be formed. The filler material is removed after the layer of microlens material has been formed.

[0012] In another embodiment of the present invention, the microlens material and the filler material comprise photoresist material. The indentations are filled with one photoresist material to form a flatter surface. A layer of microlens material is then formed and masked. The excess microlens material and the filler material may then be removed by exposing and developing. This embodiment provides for a lithographic process to fill the indentation and to remove the filler material at a later time.

[0013] In yet another embodiment of the present invention, the indentations are filled with a filler material and a layer of microlens material is formed. The microlens material is masked, exposed, and developed to define the areas that will become the microlenses. The filler material is then removed by a masking and etching process.

BRIEF DESCRIPTION OF THE DRAWINGS

[0014] For a more complete understanding of the present invention, and the advantages thereof, reference is now made to the following descriptions taken in conjunction with the accompanying drawings, in which:

[0015] FIG. 1 is a cross-section view of a wafer illustrating the formation of microlenses;

[0016] FIGS. 2a-2d are cross-section views of a wafer illustrating a process of forming microlens devices in accordance with a first method embodiment of the present invention; and

[0017] FIGS. 3a-3e are cross-section views of a wafer illustrating a process of forming microlens devices in accordance with a second method embodiment of the present invention.

DETAILED DESCRIPTION OF ILLUSTRATIVE EMBODIMENTS

[0018] The making and using of the presently preferred embodiments are discussed in detail below. It should be appreciated, however, that the present invention provides many applicable inventive concepts that can be embodied in a wide variety of specific contexts. The specific embodiments discussed are merely illustrative of specific ways to make and use the invention, and do not limit the scope of the invention.

FIGS. 2a- 2d illustrate cross-section views of a wafer during various steps of a first method embodiment of the present invention. The process begins in FIG. 2a, wherein a wafer 200 has been prepared for applying a microlens material in accordance with standard processing techniques known in the art. Generally, the wafer 200 comprises a planarization layer 212, which acts as a spacer between the microlens (not shown, to be applied in a later step) and color filters 214. The color filters 214 are located on a second planarization layer 220 that acts as a spacer between the color filters 214 and photo-diodes 216 formed in active region 218.

[0020] A bond pad area 222 provides a connection point for electrical leads, and a scribe line 224 provides a separation point between die on a wafer. Preferably, the scribe line 224 is substantially filled with a photoresist material during a previous

processing step, such as the processing steps performed in creating the planarization layer 220 and planarization layer 212.

[0021] FIG. 2b is a cross-section view of wafer 200 illustrated in FIG. 2a after the bond pad area 222 has been substantially filled with photoresist filler 232. The bond pad area 222 can be filled, for example, by applying a photoresist layer. The photoresist layer may be patterned using photolithography as is known in the art.

[0022] Preferably, the photoresist filler 232 is a positive photoresist, *i.e.*, a photoresist filler that becomes soluble in a developing solution after being exposed to an energy source, such as ultra-violet light. A mask is used to pattern the photoresist. In this embodiment in which a positive photoresist is used, the mask would prevent the photoresist filler 232 located in the bond pad area 222 from being exposed. Accordingly, the photoresist filler 232 remains after developing.

[0023] Preferably, the photoresist layer comprises a high-sensitivity positive photoresist material such as a mixture comprising poly ethylene, poly methyl methacrylate (PMMA), poly glycidol methacrylate (PGMA), propylene glycol monoethyl ether Acetate, ethylene glycol monoethyl ether acetate, cyclized 1,4-cis polyisoprene, novolak resin, metacrylate resin, cresol formaldehyde resin, ethyl lactate, and ethyl 3-ethoxypropionate.

[0024] The photoresist material is preferably deposited having a thickness substantially equivalent to the depth of the bond pad area 222. Thus, in a common design wherein the bond pad area 222 is about 1.5 to 2.0 um, the photoresist layer is preferably about 1.5 to 2.0 um in thickness.

photoresist be used. Microlens materials (discussed in greater detail below with reference to FIGS. 2c and 2d) are most commonly available as a positive photoresist. By using a positive photoresist for the microlens material and the photoresist filler 232, the process steps may be simplified and reduced. For example, by using positive photoresist for the microlens material and the photoresist filler 232, the same developing step may be used to remove both materials. Furthermore, in an alternative embodiment, a negative photoresist may be used for one or both of the microlens material and the photoresist filler 232. The use of negative photoresist, however, may require more complex processes, such as the process described below with reference to FIGS. 3a-3e.

[0026] FIG. 2c is a cross-section view of wafer 200 illustrated in FIG. 2b after a microlens layer 234 has been applied. Preferably, the microlens layer 234 is about 0.5 um to 2.0 um in thickness, but preferably is about 1.0 um thick.

[0027]. By filling the bond pad area 222 and the scribe line 224 with photoresist material prior to the application of the microlens layer, and thereby improving the topography of the wafer 200, the distribution of the microlens layer 234 is more uniform. The greater uniformity of the microlens layer 234 allows the microlens (not shown in FIG. 2c) to obtain a more uniform shape, thereby reducing or eliminating signal deviation.

FIG. 2d is a cross-section view of wafer 200 illustrated in FIG. 2c after removing the excess microlens layer 234 (FIG. 2c) and the photoresist filler 232 (FIG. 2c). In the preferred embodiment, this step includes two exposure steps and one developing step. In the first exposure step, the microlens layer 234 is masked and exposed to define the microlenses 236. Preferably, the microlens layer the exposure energy is about 60 mj to 600 mj, but most preferably is about 200 mj to 350 mj

[0029] In the preferred embodiment in which the microlens material is a positive photoresist, the mask contains opaque areas where the microlenses are to be located, thereby protecting the microlens area from being exposed and removed during developing.

[0030] In the second exposure, the photoresist filler 232 (FIG. 2c) are exposed. In the preferred embodiment, the material used for the microlens layer 234 and the TSM03-0463

photoresist fillers 232 react with different energies, and accordingly, an exposure is required to remove each of the materials. The mask for this exposure allows the photoresist filler 232 to be exposed. The photoresist filler 226, located in the scribe line 224, may remain. Alternatively, however, the photoresist filler 226 may be removed or partially removed.

[0031] The developing step removes the excess microlens material and the photoresist filler 226 located in the bond pad area 222.

[0032] Thereafter, standard processes may be utilized to reflow the microlens as is known in the art

[0033] FIGS. 3a-3e illustrate cross-section views of a wafer during various steps of a second method embodiment of the present invention. The process assumes a wafer 300 previously fabricated and prepared for fabricating microlenses. A suitable wafer 300 is discussed above in reference to wafer 200 of FIG. 2a. Thus, for the purposes of this description, the second embodiment is based upon wafer 200 of FIG. 2a, wherein like reference numerals in FIGS. 3a-3e represent the respective elements in FIG 2a.

[0034] Referring now to FIG. 3a, wherein a bond pad area 222 and scribe line 224 have been filled with photoresist fillers 232 and 226, respectively, and microlens 236 have been formed. One method of fabricating wafer 300 of FIG. 3a is described above TSM03-0463

in reference to FIGS. 2a-2d, except that the photoresist fillers 232 and 226 remain, such as may be the case if the exposure step to expose the photoresist fillers 232 and 226 was not performed as described above in reference to FIG. 2d. Generally, the following discussion presents a second embodiment of the present invention in which an alternative method is disclosed for removing the photoresist filler 232. The photoresist filler 226, located in the scribe line, may remain.

[0035] FIG. 3b is a cross-section of wafer 300 illustrated in FIG. 3b after a patterned mask 312, such as a photoresist mask, is formed. The patterned mask 312 may comprise a photoresist material. The photoresist material is masked and exposed to define the areas of the photoresist filler 232 that are to be removed, preferably via an etching process. After exposure, the photoresist material is developed to remove the exposed areas of the photoresist and expose the photoresist filler 232.

[0036] FIG. 3c is a cross-section view of the wafer 300 illustrated in FIG. 3b after the patterned mask 312 (FIG. 3b) has been exposed and developed. The wafer 300 is then subjected to an etching process to remove the exposed areas of the the photoresist filler 232. The etching process may be a wet or dry, anisotropic or isotropic, etch process, but preferably is an anisotropic dry etch process.

[0037] FIG. 3d is a cross-section view of the wafer 300 illustrated in FIG. 3c after the photoresist filler 232 (FIG. 3c) have been removed from the bond pad area 222 by the etching process discussed above. Alternatively, the photoresist filler 226 may also be removed.

[0038] FIG. 3e is a cross-section view of the wafer 300 illustrated in FIG. 3d after the patterned mask 312 (FIG. 3d) is removed. The patterned mask 312 may be removed by a wet dip in photoresist stripper, such as EKC 270 remover manufactured by EKC Technologies of Danville, CA, for example.

[0039] Thereafter, standard processes may be utilized to reflow the microlens as is known in the art.

[0040] Although particular embodiments of the invention have been described in detail, it is understood that the invention is not limited correspondingly in scope, but includes all changes, modifications, and equivalents coming within the spirit and terms of the claims appended hereto. For example, differing types of mask materials and photoresist materials may be used, negative photoresist may be used, other areas besides the bond pad area and scribe line may be filled to further alter the chip topography, and the like. Accordingly, it is understood that this invention may be extended to other

structures and materials, and thus, the specification and figures are to be regarded in an illustrative rather than a restrictive sense.